

# QUICK START GUIDE

# SV3C-DPRX

# MIPI D-PHY Analyzer

# **C** SERIES





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# Introduction

### **OVERVIEW**

The SV3C-DPRX MIPI D-PHY Analyzer is an ultra-portable, high-performance instrument that provides the most comprehensive capabilities on the market for exercising and validating MIPI D-PHY transmitter ports. Receiving traffic over a continuous range of data rates, the D-PHY Analyzer includes complete hardware LP and HS receivers, dynamic termination, and offers sophisticated capture and compare modes.

The SV3C-DPRX MIPI D-PHY Analyzer operates using the award-winning Introspect ESP Software environment, allowing for automated transmitter tests such as bit error rate, protocol timing, and MIPI conformance testing. The software also includes a full suite of tools for DSI and CSI-2 video frame extraction for packet analysis and system level test.

# **QUICK START DOCUMENTATION**

This Quick Start Guide will provide the information required for a user to get up and running with the SV3C-DPRX system. Basic hardware and software installation instructions are included, followed by a step-by-step procedure to start receiving and analyzing MIPI D-PHY signals using the Introspect ESP Software.



# Quick Start Hardware Description

# REQUIREMENTS

The full list of hardware required for this Quick Start Guide is provided below:

- 1 x SV3C-DPRX MIPI D-PHY Analyzer
- 1 x 12V 5A AC / DC power supply (Mfg: CUI, Part #: ETSA120500U)
- 1 x Personal Computer connected to the SV3C-DPRX via a USB2.0 mini B and a USB3.0 micro B cable
- 1 x MXP to SMA Cable Assembly (included with analyzer)

### HARDWARE DESCRIPTION

Figure 1 shows a diagram of the physical ports of the SV3C-DPRX.





The SV3C-DPRX module uses a single MXP connector to receive signals on its clock and data lanes. A detailed pinout of the connector is provided in Table 1 below.

| CONNECTOR   | PIN    | LANE              |
|---|--------|-------------------|
| 1 0 0 9   | 1, 2   | Rx Lane 1 (P, N)  |
|   | 3, 4   | Rx Lane 2 (P, N)  |
| $\begin{array}{c c} 4 \bigcirc \bigcirc 12 \\ 5 \bigcirc \bigcirc 13 \end{array}$ | 5, 6   | Rx Lane 3 (P, N)  |
|   | 9, 10  | Rx Lane 4 (P, N)  |
|   | 13, 14 | Clock Lane (P, N) |

#### TABLE 1: MXP CONNECTOR PINOUT



# Introspect ESP Software Installation

# SYSTEM REQUIREMENTS

The Introspect ESP Software provides an easy-to-use environment for device characterization and testplan development. To run the software, the following components are required:

- A PC installed with Windows XP, Vista, or Windows 7, 8, or 10
- The Introspect ESP install executable
- USB device drivers (refer to the driver installation instructions later in this document)

#### NOTE

A fully functional command line version of the Introspect ESP software is also available for MacOS and Linux. However, this Quick Start guide will focus on the windows version of the software.

# INTROSPECT ESP SOFTWARE INSTALLATION

#### 1. INSTALLATION PREPARATION

- a) Quit any Introspect ESP Software sessions before starting the installation.
- b) If this is your first installation of the Introspect ESP Software, open the "README\_Install.txt" file located in the installation files and install any pre-requisite components by consulting the "Windows Software Requirements" section.

#### 2. SOFTWARE INSTALLATION

- a) From the directory containing the installation files, double-click the "IntrospectESP\_Installer.exe" executable and follow the on-screen instructions.
- b) When prompted, specify the location where you want to install the Introspect ESP software. The default location is Program Files > Introspect. The software will be installed into a subfolder specifying the version number.



#### NOTE

The selected installation directory must be a new location – it cannot be the same as a previous installation.

c) By the simple press of a button, the Introspect ESP software will install its own embedded version of Python, along with its required 3rd-party modules. This means that any previous Python installations on the host computer will not be affected by the Introspect ESP software.

#### 3. INSTALL THE LICENSE FILE

- a) Towards the end of the installation, you will be asked to provide either an activation key or a license file for the software.
- b) If you have a valid activation key, simply select the "Use Activation Key" option and enter your activation key. The installer will then automatically generate the required license files.
- c) If you were provided with a license file instead, select the "Use Existing License" option, and the installer will help you copy the license file into the new installation folder.
- d) If you do not have any of the above, select the "Get a New License" option, and the installer will provide you with information that needs to be sent to Introspect Technology in order to obtain one. Copy and paste that information to request a license via: license\_support@introspect.ca.
- e) Upon receipt of the valid license files, place them into the following directory:

C:\[Your Introspect Installation Folder]\Licenses

#### NOTE

The installer creates a folder called "Introspect" under the "My Documents" folder of your account. This folder is where Test Procedures are usually saved.



🔯 Introspect ESP (v 3.6.74)

Welcome to Introspect ESP (v 3.6.74)

To narrow down the choices in the menu below, type part of the desired form factor name in the "Filter" b

introspect

Filter: Choose the IESP form factor ×

#### 4. RUNING THE INTROSPECT ESP SOFTWARE

a) Double-click on the "IntrospectESP" shortcut on your Desktop and you should see the first "welcome" window of the GUI. Please specify the hardware as "SV3C\_4L3G\_MIPI\_DPHY\_ANALYZER2" and Press "Next" to continue.

b) Select the option "Create a new Test" and click the "Ne button.

c) With a valid license in the "Licenses" directory, the following GUI screen should come up, which indicates that the Introspect software has been successfully installed.

| "Next"                                     | SVZZ_4L33_MIPI_DPHY_ANALYZER2       V         DDR_4516R       Very Attice         NMP50316C       SVIC_4123_MIPI_DPHY_GENERATOR         SVIC_4123_MIPI_DPHY_GENERATOR       SVIC_6123         SVIC_6123_0ATA_CAPTURE       SVIC_6123         SVIC_6123_0ATA_CAPTURE       SVIC_6123         SVIC_6123_0ATA_CAPTURE       SVIC_6123         SVIC_6123_0ATA_CAPTURE       SVIC_6123         SVIC_32124_0T26       SVIC_32124         SVIC_32124C126       SVIC_32124C126         SVIC_32124C126       SVIC_32124C126         SVIC_32124C126       SVIC_32124C126         SVIC_32124C126       SVIC_4165_MIPI_CPHY_GENERATOR         SVIC_4165_MIPI_DPHY_GENERATOR       SVIC_4165_MIPI_DPHY_GENERATOR         SVIC_4165_MIPI_DPHY_GENERATOR       SVIC_4165_MIPI_DPHY_GENERATOR |
|--|---|
|  | Create a new Test   |
|  | O Open an existing Test folder  |
|  | Onen most recent Test folder  |
|  | Next >  |
| B Introspect ESP (v 3.6.74) - Untitled (SV | 3C_4L3G_MIPI_DPHY_ANALYZER2) - 🗆 X  |
| File Edit IESP/MIPI_DPHY_ANALY2<br>Params  | ER2 Wizards ControlPanels Tools Results Help<br>Log Results   |
| Components                                 | laneList1 properties (class: MpiDphyLaneList)   |
| taneList1<br>mipiClockConfig1              | lanes [1-4]<br>in Data Solit Annual Janas Terra   |
| mipiProtocol                               | lpDataThresholdVotages [600.0]  |
|  | IpCiock Intesnoid Voltage 600.0<br>hsData Threshold Voltages [50.0]   |
|  | hsClockThresholdVoltage 50.0  |
|  | data:cquarzationDcGains [U]<br>clock.EqualizationDcGain 0   |
|  | lans  |
| Add Remove Contra                          | list of RX lane numbers (e.g. [1,2,3])  |
| Test Develop                               |   |
| 1 miniClockConfig1 acture                  | ,   |
| i mipiClockConfigl.setup(                  | 1   |
| MIPI DPHY Cei2_v1_3                        | Run   |



#### 5. FURTHER DOCUMENTATION

The "[IntrospectESP\_install\_dir]\Doc" folder contains the following information on the software:

- "IntrospectESP\_UserManual.pdf" is the user manual for the Introspect ESP Software and is recommended reading for all users.
- "svt.html" and "iesp.html" provide documentation on the Python component classes and lower-level functions specific to the selected form factor. Both files can be found in "C:\[IntrospectESP\_install\_dir]\Doc\FormFactors\SV3C\_4L3G\_MIPI\_DPHY\_ANALYZER2". These are intended for intermediate and advanced users.

#### NOTE

Both the user manual and the above html files are also conveniently available from the "Help" pull down menu located on the top right of the main Introspect ESP Software window.

"Application Notes" can also be found in the "C:\[IntrospectESP\_install\_dir]\Doc" sub-folder and have more advanced features, often in the form of tutorials.



# **USB** Driver Installation

The following procedure will allow for automated FTDI driver installation.

1. HARDWARE SETUP

For this procedure, connect the SV3C-DPRX to the PC using both a USB2.0 mini B and a USB3.0 micro B cable, as shown in Figure 2 below, and power on the module. To allow for driver installation, the PC should be connected to the internet as well.



### 2. WAIT FOR NEW HARDWARE DETECTION

The PC should display the message "New drivers successfully installed" once the installation process is complete. If this does not occur, see the troubleshooting notes at the end of this section.

### 3. VERIFY DRIVER INSTALLATION

 a) If it is not still open, launch the Introspect ESP Software and select the "SV3C\_4L3G\_MIPI\_DPHY\_ANALYZER2" form factor. From the main GUI window, click the "IESP/MIPI\_DPHY\_ANALYZER2" drop down menu and click "Connect", as shown here. Establishing the connection should take a couple of seconds.



b) To verify the connection, select the "IESP/ MIPI\_DPHY\_ANALYZER2" drop down menu and click "Status". A dialog window should confirm that the SV3C-DPRX module is connected, as shown here. Also, the status indicator in the bottom left corner of the main GUI window should be solid green, indicating that the SV3C-DPRX unit is connected and ready.

| IESP/SV3C_4L3G_MIPI_DPHY_ANALYZER2 Status |                  |  |
|---|------------------|--|
| Personality:                              | FWSV3CDPRX04B020 |  |
| Connected:                                | True             |  |
| Temperature:                              | 45               |  |
| Status:                                   | 0000000          |  |
|   |                  |  |
|   |                  |  |
|   |                  |  |
|   |                  |  |
|   |                  |  |

#### 4. TROUBLESHOOTING

If the connection cannot be established, or if the drivers cannot be found or automatically installed, please refer to the "FTDI Driver Manual Installation" Appendix to install the required drivers.



# SV3C-DPRX Demonstration

### STEP-BY-STEP GUIDE: RECEIVING A D-PHY CSI-2 IMAGE FRAME

The following step-by-step guide will allow the user to set up the SV3C-DPRX module in order to receive a CSI-2 image frame being transmitted over a single D-PHY lane. It will also demonstrate how to capture, visualize, and analyze the received packets using the mipiDphyCsiDataCapture component of the Introspect ESP Software. The following procedure is intended to provide an overview of how to use the Introspect ESP Software GUI and highlight several of its key features.

#### 1. CONNECT THE HARDWARE COMPONENTS

In order to analyze a D-PHY signal, you will need a DUT capable of producing CSI-2 image frames, such as a camera. Connect your DUT's differential clock and data signals to the MXP Rx port of the SV3C-DPRX module according to the pinout shown previously in Table 1.

#### 2. GETTING TO KNOW THE INTROSPECT ESP SOFTWARE GUI

a) If you have not done so previously during the USB driver installation procedure, launch the Introspect ESP Software, select the "SV3C\_4L3G\_MIPI\_DPHY\_ANALYZER2" form factor and create a new test procedure. Connect the SV3C-DPRX to your PC using both a USB2.0 mini B cable and a USB3.0 micro B cable and power up the module.

### NOTE

The USB3.0 port is used to accelerate data transfers between the SV3C-DPRX and the host PC. A USB3.0 connection is not mandatory for operating the module, but it is highly recommended, especially when capturing large image frames.



 b) In the top left corner of the main GUI window, select the "IESP/ MIPI\_DPHY\_ANALYZER2" drop down menu and click the "Connect" option. Establishing connection should take a couple of seconds.

| Connect                                 | ZERZ WIZING CON  | rolPanels Tools  | Results Help  |   |  |   |  |
|---|--|--|---|---|--|---|--|
| connect                                 |  |  | Results   |   |  |   |  |
| Disconnect                              |  |  | rties (class: MipiDphyl   | LaneList)   |  |   |  |
| Run Test                                |  | F5   | [1-4]   |   |  |   |  |
| Run Test After Dela                     | y  |  | True  |   |  |   |  |
| Status                                  |  |  | [600.0]   |   |  |   |  |
| Reset                                   |  |  | [50.0]  |   |  |   |  |
| Options                                 |  |  | 50.0  |   |  |   |  |
| Change Form Facto                       | or to: SV3C_4L3G_MIPI_CP   | PHY_ANALYZER2  | [0]   |   |  |   |  |
|   | lanes<br>list of RX lane numbers (e  | e.g. [1,2,3])  |   |   |  |   |  |
| Remove Config                           |  |  |   |   |  |   |  |
| Remove Config                           |  |  |   |   |  |   |  |
| Remove Config                           |  |  |   |   |  |   |  |
| Remove Config<br>e<br>lockConfigl.setup | 0  |  |   |   |  |   |  |
| Remove Config<br>e<br>lockConfigl.setup | 0  |  |   |   |  |   |  |
| Remove Config<br>e<br>lockConfigl.setup | 0  |  |   |   |  |   |  |
|   | Run Test<br>Run Test After Dela<br>Status<br>Reset<br>Options<br>Change Form Facto | Run Test<br>Run Test Ater Delay<br>Status<br>Reset<br>Options<br>Change Form Factor to: SV3C_4L3G_MIPLCF | Run Tet F5 Run Tet After Delay Status Reset Options Change Form Factor to: \$V3C_4L3G_MIPI_CPHY_ANALYZER2 | Run Tet         F5         [1-4]           Run Tet After Delay         5           Status         1000         1000           Rest         500         500           Options         0         500           Change Form Factor to: \$V3C_4L3G_MIPI_CPHY_ANALIVZER2         101 | Run Text         F5         Text and the Delay           Run Text After Delay         Text         Text           Status         600.01         500.01           Rest         50.0         150.01           Options         50.0         101           Change Form Factor to: SV3C_4L3G_MIPLCPHY_ANALVZER2         101         0 | Run Tett         F5           Run Tett         F5           Run Tett After Delay         F6           Status         F600 (0)           Rest         500 (0)           Options         500 (0)           Change Form Factor to: 5VSC_4L3G_MIPI_CPHY_ANALVZER2         0 | Run Tett         F5           Run Tett         F5           Run Tett         F5           Status         F6           Rest         F000           Options         50.0           Change Form Factor to: \$V3C_4L3G_MIPI_CPHY_ANALIVZER2         F0 |

c) To verify the connection between the PC and the SV3C-DPRX module, select the "IESP/ MIPI\_DPHY\_ANALYZER2" drop down menu and click the "Status" option. A dialog window should confirm that the SV3C-DPRX module is connected, as shown here, and will list the detected personality / firmware version.

| IESP/SV3C_4L3G_MIPI_DPHY_ANALYZER2 Status |                  |  |
|---|------------------|--|
| Personality:                              | FWSV3CDPRX04B020 |  |
| Connected:                                | True             |  |
| Temperature:                              | 45               |  |
| Status:                                   | 0000000          |  |
|   |                  |  |
|   |                  |  |
|   |                  |  |
|   |                  |  |
|   |                  |  |

- d) By default, when started in the "SV3C\_4L3G\_MIPI\_DPHY\_ANALYZER2" form factor, the GUI contains a single line of code in the "Test Procedure" pane and three components in the "Components" section, as shown below.
- e) The mipiClockConfig1 component is used to specify the rate at which data is being transmitted to the SV3C-DPRX MIPI D-PHY Analyzer. Before continuing, make sure the "dataRate" property matches the data rate of the transmitting DUT, or use the "autoDetectClock" functionality. The mipiClockConfig1 component can also be used to configure external clock signals to synchronize the SV3C-DPRX module to other systems.



#### SV3C-DPRX DEMONSTRATION



#### NOTE

An external clock reference must be used to capture HS-only data signals.



f) The "laneList1" component is used to configure the data and clock lanes that will be used to capture D-PHY signals using the SV3C-DPRX MIPI D-PHY Analyzer. The SV3C-DPRX supports any combination of data lanes, allowing for greater flexibility when testing multiple types of DUTs.

| 🔯 Introspect ESP (v 3.6.74) - Untitled (S                       | V3C_4L3G_MIPI_DPHY_ANALYZER   | 2)  | - 0    | × |  |
|---|---|---|--------|---|--|
| File Edit IESP/MIPI_DPHY_ANALY                                  | ZER2 Wizards ControlPanels  | Tools Results Help  |        |   |  |
| Params  | Log   | Results   |        |   |  |
| Components  |   | laneList1 properties (class: MipiDphyLane                       | eList) |   | isDataSplitAcrossLapes is used to  |
| Add Remove Config<br>Test Procedure<br>1 mipiClockConfig1.setup | lanes<br>isDataSplitAcrossLanes<br>IpDataThresholdVoltages<br>IpCockThresholdVoltage<br>hsDataThresholdVoltage<br>hsCockThresholdVoltage<br>dataEqualizationDcGains<br>clockEqualizationDcGain<br>late of RX lane numbers (e.g. [1,2,3] | (1-4)<br>True<br>[600.0]<br>600.0<br>[50.0]<br>50.0<br>[0]<br>0 |        |   | isDataSplitAcrossLanes is used to<br>specify if the received data packets<br>are split across the data lanes<br>All threshold settings<br>are fully configurable |
| MIPI DPHY Csi2_v1_3   |   | Run   |        |   |  |

g) In this first example, only a single D-PHY lane will be used to transmit data from the DUT to the SV3C-DPRX MIPI D-PHY Analyzer. To use only lane 1 of the SV3C-DPRX, select the "laneList1" component in the left view, then select the "lanes" parameter in the right view and edit it to: [1].



#### NOTE

To modify a component property, a user can either manually edit the corresponding field in the "Properties" view, or programmatically via the "Test Procedure" editor tab. For example, if a user only wants to use Lane 1, they can add the following line to the "Test Procedure" tab:

laneList1.lanes = [1]

This allows on-the-fly change of the attributes during a test run for greater flexibility.

h) The "mipiProtocol" component is used to specify the encoding specifications of the received data packets, as shown below. The SV3C-DPRX module fully supports the DSI, DSI-2, and CSI-2, as well as the reception of bitstreams without any type of encoding.

| 🔯 Introspect ESP (v 3.6.74) - Untitled (SV                       | V3C_4L3G_MIPI_DPHY_ANALYZER  | 2)  | - C | x i |  |
|--|--|---|-----|-----|--|
| File Edit IESP/MIPI_DPHY_ANALYZ                                  | ZER2 Wizards ControlPanels   | Tools Results Help                          |     |     |  |
| Params   | Log  | Results                                     |     |     |  |
| Components   |  | mipiProtocol properties (class: MipiProtoco | )   |     | Both protocol type and protocol  |
| Add Remove Config<br>Test Procedure<br>1 mipiClockConfig1.setup( | protocol<br>csiVersion<br>csiScramble<br>protocol<br>MIPI protocol | CSI<br>Csi2_v1_3<br>False                   |     |     | Both protocol type and protocol<br>version can be specified from the<br>mipiProtocol component |
|  |  |   |     |     |  |
| MIPI DPHY Csi2_v1_3  |  | Run   |     |     |  |



i) In this example, the transmitted data packets are encoded using the CSI-2 v1.3 protocol, so the default settings will do.

### NOTE

If your DUT uses another version of the CSI-2 protocol, make sure to edit the "mipiProtocol" component as described previously to match your device's settings.

### 3. ADDING TEST COMPONENTS

a) The objective of this step-by-step guide is to capture a CSI-2 encoded image frame over a single D-PHY lane. In order to capture an image frame, the "CsiDataCapture" component must be added to the test procedure. This component is responsible for capturing, displaying, and analyzing data packets received by the SV3C-DPRX module.

To add a component to the test procedure, click the "Add" button in the left "Components" view of the main GUI window. In the "Add Component" window, select the "CsiDataCapture" component, and click the "Add Component" button, as shown below.





b) A new "csiDataCapture1" component will be added to the left "Components" view of the main GUI window. Notice that a new line of code has automatically been added to the end of the "Test Procedure" editor window in order to start the data capture, as shown below.



| 🔯 Introspect ESP (v 3.6.74) - Untitled (  | SV3C_4L3G_MIPI_DPHY_ANALYZER2)   |  | – 🗆 X |  |
|---|--|--|-------|--|
| File Edit IESP/MIPI_DPHY_ANAL   | ZER2 Wizards ControlPanels T   | fools Results Help   |       |  |
| Params  | Log *  | Results  |       |  |
| Components  | csiDataCap   | oture1 properties (class: MipiDphyCsiDataCapture   | e)    |  |
| CEIDataCapture1<br>laneList1<br>mipICockCorfig1<br>mipiProtocol   | laneList<br>lanes<br>captureMode<br>triggerCondition<br>preTriggerDuration<br>postTriggerDuration<br>ignoreHsData<br>caiRawFormatBayerCell<br>timeout<br>csiEpd<br>wantAnalysis<br>imageFileFormat<br>pixeIDataFileMode<br>saveResults | I<br>burst<br>frameStart<br>1e+04<br>numberOfFrameEnds<br>1<br>False<br>BGGR<br>20<br>False<br>True<br>PNG<br>NONE<br>True |       | All frame and triggering parameters are<br>configurable<br>Captured data packets can be analyzed<br>and saved to the host computer |
| Add Remove Config<br>Test Procedure<br>1 mipiClockConfigl.setup<br>2 csiDataCapturel.run()<br>O MIPI DPHY Csi2_v1_3 | IaneList<br>LaneList component<br>()<br>Ru   | n  |       | — A single line starts the data capture  |

- c) The default configuration of the "CsiDataCapture" component will capture a whole CSI-2 data frame from beginning to end. If you want to capture different types of data, for example a single data packet, you can do so by editing the "triggerCondition", "postTriggerType" and "postTriggerDuration" properties to fit your needs.
- d) The "CsiDataCapture" component requires a list of lanes to be specified in order to capture data packets. To do so, select the "laneList" property of the csiDataCapture1 component, click the arrow from the drop-down menu on the right-hand side, and choose the desired "laneList" component from the list. Since the current test procedure only contains a single "laneList" component, select "laneList1" as shown below.



| File                  | Edit IESP/IMIPI_DPHY_ANA | LYZERZ WIZards ControlPanels | Ioois Results Help                       |              |  |
|-----------------------|--------------------------|------------------------------|--|--------------|--|
|                       | Params                   | Log *                        | Results                                  |              |  |
|                       | Components               | csiData                      | Capture1 properties (class: MipiDphyCsiD | lataCapture) |  |
| siData(               | Capture1                 |                              |  |              |  |
| ane Lisi<br>aini Cloc | kConfig1                 | lanes                        | laneList1                                |              |  |
| nipiCicc              | otocol                   | captureMode                  | None                                     |              |  |
|                       |                          | triggerCondition             | Deraut Value                             |              |  |
|                       |                          | preTriggerDuration           | 1e+04                                    |              |  |
|                       |                          | postTriggerType              | numberOfFrameEnd                         | ds           |  |
|                       |                          | postTriggerDuration          | 1  |              |  |
|                       |                          | ignoreHsData                 | False                                    |              |  |
|                       |                          | csiRawFormatBayerCell        | BGGR                                     |              |  |
|                       |                          | timeout                      | timeout 20                               |              |  |
|                       |                          | csiEpd False                 |  |              |  |
|                       |                          | wantAnalysis                 | True                                     |              |  |
|                       |                          | imageFileFormat              | PNG                                      |              |  |
|                       |                          | pixelDataFileMode            | NONE                                     |              |  |
|                       |                          | saveResults                  |  |              |  |
|                       |                          | laneList                     |  |              |  |
| Add                   | I Remove Config          | LaneList component           |  |              |  |
| Test Pr               | rocedure                 |                              |  |              |  |
|                       |                          | - 0                          |  |              |  |
| 1 m                   | upiClockConfigl.setu     | P()                          |  |              |  |
| 2 c                   | siDataCapturel.run()     |                              |  |              |  |
|                       |                          |                              |  |              |  |
|                       |                          |                              |  |              |  |
|                       |                          |                              |  |              |  |

### 4. EXECUTING THE TEST PROCEDURE

a) Now that the hardware has been connected and the procedure has been configured to fit the DUT's settings, the SV3C-DPRX is ready to receive D-PHY CSI-2 image frames. Before starting the procedure, make sure the DUT is transmitting data on lane 1. Once data is being sent to the SV3C-DPRX module, the test procedure can be executed from the GUI.



 b) To do so, click the "Run" button at the bottom of the main GUI window as shown here, or use the F5 shortcut key.

| Introspect ESP (v 3.6.74) - Untitled (           | 5V3C_4L3G_MIPI_DPHY_ANALYZER2)                           |  | - | $\times$ |
|--|--|--|---|----------|
| File Edit IESP/MIPI_DPHY_ANAL                    | ZER2 Wizards ControlPanels Tools                         | Results Help                             |   |          |
| Params   | Log *  | Results                                  |   |          |
| Components                                       | csiDataCapture1 pr                                       | operties (class: MipiDphyCsiDataCapture) |   |          |
| csiDataCapture1<br>laneList1<br>mipiClockConfig1 | lane List<br>lanes                                       | laneList1<br>[1]                         |   |          |
| mipiProtocol                                     | captureMode<br>triggerCondition                          | burst<br>frameStart                      |   |          |
|  | pre TriggerDuration<br>post TriggerType                  | 1e+04<br>numberOfFrameEnds               |   |          |
|  | post TriggerDuration<br>ignoreHsData                     | 1<br>False                               |   |          |
|  | csiRawFormatBayerCell                                    | BGGR<br>20                               |   |          |
|  | csiEpd<br>wart Apalurie                                  | False                                    |   |          |
|  | imageFileFormat PNG                                      |  |   |          |
|  | saveResults True   |  |   |          |
| Add Remove Config                                | Ianes<br>The lanes used by the TaneList' (shown here for | convenience)                             |   |          |
| Test Procedure 1 mipiClockConfigl.setup          | 0  |  |   | _        |
| 2 csiDataCapturel.run()                          |  |  |   |          |
| MIPI DPHY Csi2_v1_3                              | Run  |  |   |          |

c) After clicking the "Run" button, the GUI will automatically change from the "Params" tab to the "Log" tab where you can find information about the currently running procedure. Any errors occurring during the test run will be reported here.

| Controspect ESP (v 3.6.74) - Untitled (SV3C_4L3G_MIPI_DPHY_ANALYZER2)   | - |    | ×   |
|---|---|----|-----|
| File Edit IESP/MIPI_DPHY_ANALYZER2 Wizarde ControlPanels Tools Results Help   |   |    |     |
| Params Log Results  |   |    |     |
| <pre>*** Logging to file: C:\Users\Philippe\AppData\Local\Temp\tmpn_zipf3\Logs\log !625.tx *** Starting Test 'Unitited' 2019-08-08_1625_12 Components used by Test Procedure: [cslDataCapture], laneList], mipiClockConfid Starting flats Capture (cslDataCapture]) Test Sinis for trigger Test finished Test took 14.8 seconds </pre> |   | 8_ | < × |
| MIPI DPHY Cai2_v1_3 Run   |   |    |     |

# NOTE

The Python print() function will automatically print out messages in the "Log" tab during the test run. This can be used for debugging as well as keeping track of test procedure execution.



 d) After the test procedure has completed, the GUI will automatically change to the "Results" tab. The captured image frame can be previewed by selecting the most recent test run result on the left side of the window and hovering above the "csiDataCapture1" icon.



e) As can be seen, the SV3C-DPRX module was able to properly capture the Introspect Technology logo encoded using the CSI-2 protocol and transmitted over a single D-PHY lane.

All packets received during the data capture can be visualized by double clicking the "csiDataCapture1" icon under the "Results" tab. The received data can be arranged into "HS Bursts", "CSI Packets" or "Frames" by navigating the "DPHY DataCapture" window. "LP States" and "LP Events" are also accessible to allow easy inspection of LP to HS transitions of the transmitting DUT.



| 🔯 ОРНУ  | 🙆 DPHY DataCapture: Run_2019-08-08_1625 / csiDataCapture1 – 🛛 |                       |           |           |               |              |             |              |             |                             | ×         |        |     |
|---|---|-----------------------|-----------|-----------|---------------|--------------|-------------|--------------|-------------|-----------------------------|-----------|--------|-----|
| burst mode: 416 bursts, 416 CSI packets, 1249 lpStates, 832 lpEvents, 1 frame |   |                       |           |           |               |              |             |              |             |                             |           |        |     |
| ● lane 2 ○ lane 3 ○ lane 4 Go To: Timestamp ∨ Times: relative To Start ∨      |   |                       |           |           |               |              |             |              |             |                             |           |        |     |
| HS Bursts   | CSI Packets   | LP States             | LP Events | Frames    |               |              |             |              |             |                             |           |        |     |
| Burst ID  | Time (ms)   | NumBytes              | NumBits   | SotOffset | NumCsiPackets |              |             |              |             |                             |           |        | ^   |
| 0   | 20.927610   | 4                     | 472       | 326       | 1             |              |             |              |             |                             |           |        |     |
| 1   | 20.928895   | 2628                  | 21464     | 326       | 1             |              |             |              |             |                             |           |        |     |
| 2   | 20.957610   | 2628                  | 21464     | 327       | 1             |              |             |              |             |                             |           |        |     |
| 3   | 20.987605   | 2628                  | 21472     | 335       | 1             |              |             |              |             |                             |           |        |     |
| 4   | 21.017605   | 2628                  | 21472     | 335       | 1             |              |             |              |             |                             |           |        |     |
| Burst 0 De  | etail   |                       |           | 1         |               |              |             |              |             |                             |           |        |     |
| Offset: 326   |   | <<                    | <<        | <         | >             | >> >>        | SOT         | Find         |             |                             |           |        |     |
|   | bits:00   | 011101 <mark>0</mark> | 000000    | 000000    | 00000000000   | 000000000111 | 11111111111 | 111111111111 | 11111111111 | 1111111 <mark>1111</mark> 1 | 111111111 | 111111 | .11 |
| byte  | index:  |                       |           | 0         | 1             | 2 3          |             |              |             |                             |           |        |     |
| byte  | (hex):  |                       | C C       | 0         | 00 0          | 0 00         |             |              |             |                             |           |        |     |
| byte  | (aec):  |                       |           | U         | U             | 0 0          |             |              |             |                             |           |        |     |

# NOTE

The "DPHY DataCapture" result window allows for easy failure analysis by highlighting frames where errors have been detected.

This concludes the SV3C-DPRX Quick Start demonstration. For further information, please consult the Introspect ESP GUI user manual from the "Help > User Manual" pull down menu of the main GUI window.



# Appendix

# FTDI DRIVER MANUAL INSTALLATION

The Introspect ESP Software communicates with the SPI Controller via an FTDI device (connected via USB). If you don't already have required FTDI drivers installed on your Windows computer, or if the automated driver detection presented earlier in this document was unsuccessful, you will need to download them from the FTDI web site. To do this, follow the instructions found at

http://www.ftdichip.com/Documents/InstallGuides.htm

The latest drivers can be found at

http://www.ftdichip.com/Drivers/D2XX.htm

Note that the driver version used in our product development is 2.12.

You may wish to use the "usbview" utility program linked to on the following FTDI page:

http://www.ftdichip.com/Resources/Utilities.htm

This program will allow you to check that your computer can "see" the FTDI device over USB.



| Revision Number | History          | Date           |
|-----------------|------------------|----------------|
| 1.0             | Document Release | August 8, 2019 |

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